

# Arun Mehta

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Santa Clara, CA

## Experience

Securitas, San Jose, CA  
February 2024 - Current  
Security Officer

Working as a screening officer in the data center. Responsible for following SOP(Standard Operating Procedure) to protect the data. Document the activities and the incidents in the shift report in accordance with the SOP.

Persenc Software, Sunnyvale, CA  
February 2005 - Current  
Owner

Encrypted email providing end-to-end encryption based on industry standards, OpenPGP, RSA and AES. Implementation using Go programming language with web user interface. Previous versions were implemented using Java with Swing graphical user interface.

Neso Technology, Santa Clara, CA  
January 1998 - October 2001  
Owner

Website development using HTML, CSS and Javascript. Dynamic web application development using MySQL and PostgreSQL databases.

MotelValues Online, Santa Clara, CA  
November 1995 - December 1997  
Owner

Web advertising service focusing on the motel market segment.

Quickturn Design Systems, Inc. Mountain View, CA  
October 1995 - November 1995  
Product Marketing Engineer

Product management for Logic Analyzer emulator and interface products. Beta testing for HDL-ICE product.

Silicon Graphics Inc. / MIPS Technologies Inc. Mountain View, CA  
June 1991 - September 1995  
MTS, Technical Marketing

Product specialist for R4400/R4000 microprocessors. Responsible for managing strategic alliances with semiconductor and system partners. Responsible for communicating technical and business information to global partners. Participated in trade shows.

Micro Computing Solutions, Santa Clara, CA  
October 1990 - June 1991  
Owner

PC hardware and software reseller for both Windows/DOS and UNIX market.

### MIPS Computer Systems, Sunnyvale, CA

June 1989 - September 1990

Member of Technical Staff

Fault simulation for the R3010 floating-point processor. Evaluated initial fault coverage and implemented additional tests for improving the coverage.

### Intel Corporation, Santa Clara, CA

May 1984 - May 1989

Senior Engineer

Participated in Random Instruction Testing for the i860 microprocessor. Involved in customer interface, design methodology, logic simulation, place and route and interface with manufacturing for ASIC gate array business. Developed and used UNIX based automation tools for i386 production test development. Participated in debugging of production test program and developed diagnostic programs in x86 assembly language for functional verification of i386.

### Martek Inc. Westmont, IL

January 1984 - April 1984

Software Engineer

Worked on a PC-based integrated package for small to medium size PCB manufacturing operation.

## Education

### Executive MBA, University of San Francisco, San Francisco, CA

August 1993 - June 1995

A team oriented approach to management education for experienced professionals.

### University of Texas at Austin, Austin, TX

September 1980 - December 1983

Graduate study in Electrical Engineering with focus on robotics.

### MSEE, Illinois Institute of Technology, Chicago, IL

June 1979 - August 1980

Master's degree in Electrical Engineering with emphasis on digital systems design.

### BE(EE), Maharaja Sayajirao University, Vadodara, India

June 1973 - June 1978

Bachelor's degree in Engineering with broad Electrical Engineering curriculum.

## Computing Skills

Go programming

HTML5, CSS3 and JavaScript

Java, C, Python

Linux, Bash scripting

Assembly language programming

## **Security Skills and Certifications**

Problem-solving

Communication and interpersonal skills

Report writing

California Guard Card (Registration)

Certified in CPR/AED/First Aid